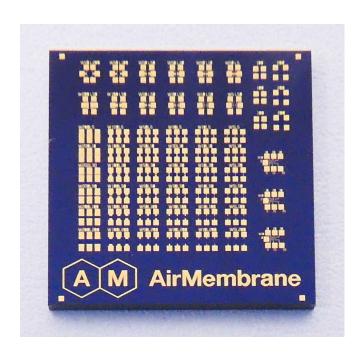


Graphene Field-Effect Transistor FR01

FR01 is a graphene Field Effect Transistor (FET) chip made from highly clean chemical vapor deposition (CVD) graphene and advanced transfer and device fabrication technology. This FET chip is ideal for usages such as developing various sensors using graphene and confirming the potential of graphene by measuring electrical characteristics. It is back-gate type FET which uses a low-resistance silicon substrate as the gate electrode. Four types of devices, two-terminal, vdP, Hall bar, and TLM, with single-layer graphene as the channel material, are incorporated in a chip of 10mm x 10mm. Since graphene channel is not encapsulated, experiments such as functional modification can be performed.

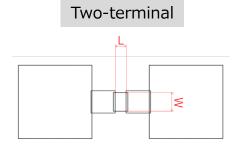
Applications: Development of graphene devices, chemical sensors, gas sensors, magnetic sensors, etc.



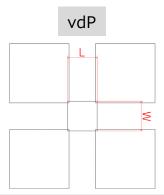
| Chip size | 10mm×10mm |
|--|----------------------------------|
| Gate oxide material, thickness | SiO2、100nm |
| Substrate material, thickness, resistivity | Si、525μm、<10Ωcm |
| Electrode metal | Au |
| Dirac point | <30V |
| Mobility | >1000cm ² /Vs |
| Devices | Two-terminal, vdP, Hall bar, TLM |

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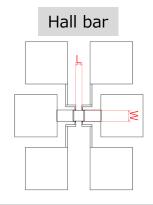
A M AirMembrane



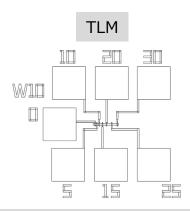
| W [µm] | L [µm] | Quantity |
|--------|-------------------------|----------|
| 5 | 5, 10, 30, 50, 100, 200 | 3 each |
| 10 | 5, 10, 30, 50, 100, 200 | 3 each |
| 30 | 5, 10, 30, 50, 100, 200 | 3 each |
| 50 | 5, 10, 30, 50, 100, 200 | 3 each |
| 100 | 5, 10, 30, 50, 100, 200 | 3 each |
| 200 | 5, 10, 30, 50, 100, 200 | 3 each |



| W [µm] | L [µm] | Quantity |
|--------|--------|----------|
| 100 | 100 | 3 |
| 200 | 200 | 3 |



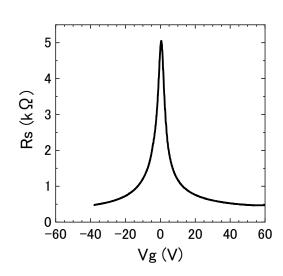
| W [μm] | L [µm] | Quantity |
|-------------------------|-------------------------|----------|
| 50 | 5, 10, 30, 50, 100, 200 | 1 each |
| 5, 10, 30, 50, 100, 200 | 50 | 1 each |



| W [µm] | L [μm] | Quantity |
|--------|-----------------------|----------|
| 10 | 5, 10, 15, 20, 25, 30 | 3 |

Typical characteristics (right figure)

Dependence of sheet resistance (Rs) on gate voltage (Vg) measured with vdP device (W100 μ m, L100 μ m) at room temperature in vacuum.



AirMembrane Corporation

Tsukuba Center Inc., 2-1-6 Sengen, Tsukuba, Ibaraki, 305-0047, Japan

Phone +81-29-869-6551

www.airmembrane.co.jp

FAX +81-29-869-6552

sales@airmembrane.co.jp